

Item: PM2

PCI-E 1X to mini pci express adapter

Reference Schematic Rev. 2.02

Schematic update note:

First released.....2009/May/11 V2.01

Second released.....2010/May/11 V2.02

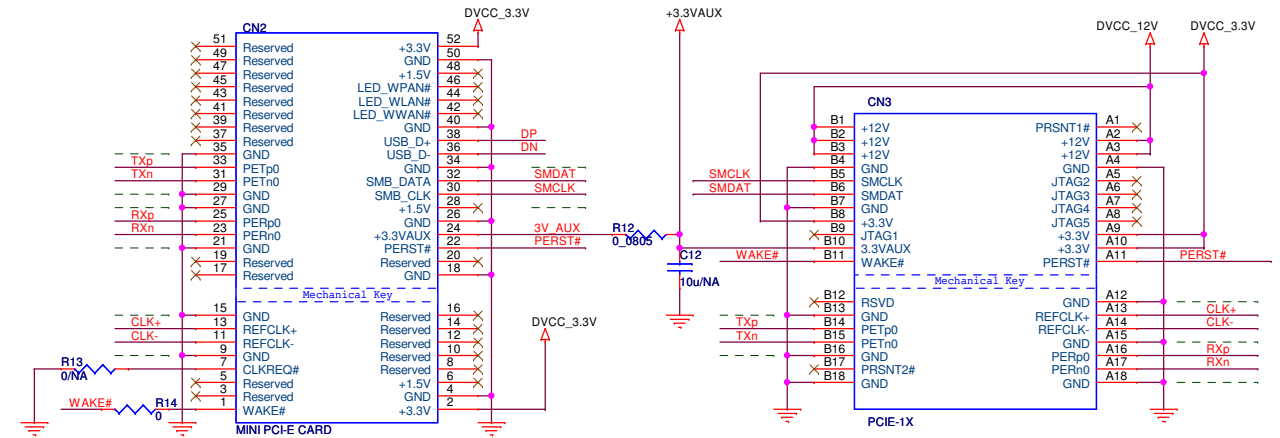
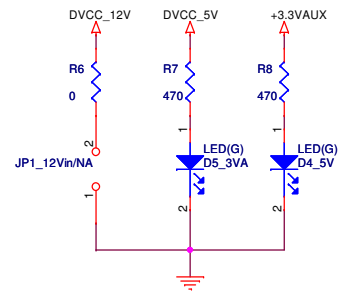
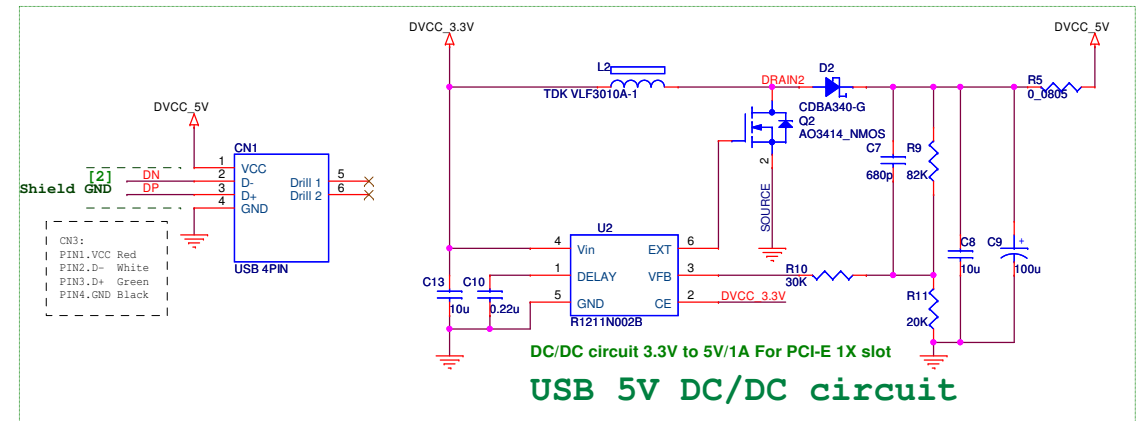
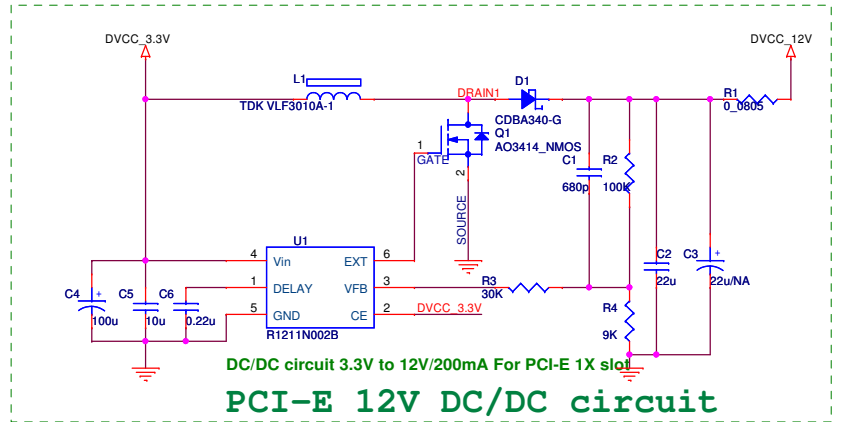
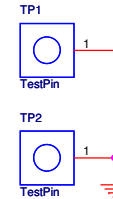
JP1/NA, C3/NA, C12/NA , C2 from 10u to 22u

PCB Layout Guideline

RXp and RXn are a 100 ohm differential impedance pair (50 ohm single ended) and must be length matched with 5 mils.

(CTXn + TXn) and (CTXp + TXp) are a 100 ohm differential impedance pair (50 ohm single ended) and must be length matched with 5 mils. i.e. CTXp must be within 5 mils of CTXn, TXp must be within 5 mils of TXn, and (CTXp + TXp) must be within 5 mils of (CTXn + TXn).The coupling capacitors must be placed as close to the PCI Express Edge connector as possible.

Card dimensions and design should match requirements for the standard half length PCI-Express card and should be designed to use the card retainer as detailed in the PCI Express Electromechanical specification rev 1.0a. Height may be increased to allow for placement of all components but width and thickness must be as required in the specification



Title		
PCI-E 1x to Mini PCI-E adapter		
Size	Document Number	Rev
A3	MINI PCI-E CARD	2.02
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Bplus technology Co., Ltd.		